

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

IDL-HO LEE

Application No.:

Filed:

For: **SYNCHRONOUS SEMICONDUCTOR
MEMORY DEVICE HAVING CLOCK
SYNCHRONIZATION CIRCUIT AND
CIRCUIT FOR CONTROLLING ON/OFF
OF CLOCK TREE OF THE CLOCK
SYNCHRONIZATION CIRCUIT**

Art Group:

Examiner:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Sir:

Enclosed herewith for filing in the above-identified U.S. Patent Application are the formal drawings, 5 sheets including 6 Figures. Please charge any additional fees or credit any overpayment to Deposit Account No. 02-2666. A duplicate copy of the Fee Transmittal is enclosed for this purpose.

Respectfully submitted,

Blakely, Sokoloff, Taylor & Zafman LLP

Dated: 7/21/03


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